

## Abstract of Disclosure

[illegible]

## Figures

Figure 1: A schematic diagram of the experimental setup. The diagram shows a cross-section of a system with a central region labeled 'Sample' and two outer regions labeled 'Electrode'. The 'Sample' region is connected to a 'Power Source' and a 'Measurement System'. The 'Electrode' regions are connected to a 'Ground' and a 'Measurement System'. The diagram also shows a 'Current' flowing through the system and a 'Voltage' being measured across the 'Sample' region.